

# NEO-M9N

## u-blox M9 standard precision module

### Data Sheet



### Abstract

This data sheet describes the NEO-M9N module which provides concurrent reception of up to four GNSS (GPS, Galileo, GLONASS, BeiDou) in a 12.2 x 16.0 mm package.

# Document information

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<b>Engineering Sample</b>	Advance Information	Data based on early testing. Revised and supplementary data will be published later.
<b>Initial Production</b>	Early Production Information	Data from product verification. Revised and supplementary data may be published later.
<b>Mass Production / End of Life</b>	Production Information	Document contains the final product specification.

This document applies to the following products:

<b>Product name</b>	<b>Type number</b>	<b>Firmware version</b>	<b>PCN reference</b>
NEO-M9N	NEO-M9N-00B-00	SPG 4.00	N/A

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# 1 Functional description

## 1.1 Overview

The NEO-M9N module is built on the robust u-blox M9 GNSS chip, which provides exceptional sensitivity and acquisition times for all L1 GNSS systems. The u-blox M9 standard precision GNSS platform, which delivers meter-level accuracy, succeeds the well-known u-blox M8 product range.

The receiver also provides higher navigation rate and improved security features compared to previous u-blox GNSS generations.

The NEO-M9N module is available in the NEO form factor LLC package.

## 1.2 Performance

Parameter	Specification	
Receiver type	Multi-constellation GNSS standard precision receiver	
Accuracy of time pulse signal	RMS	30 ns
	99%	60 ns
Frequency of time pulse signal	0.25 Hz to 10 MHz (configurable)	
Operational limits <sup>1</sup>	Dynamics	≤ 4 g
	Altitude	80,000 m
	Velocity	500 m/s
Velocity accuracy <sup>2</sup>	0.05 m/s	
Dynamic heading accuracy	0.3 deg	

GNSS		GPS+GLO+GAL+BDS	GPS+GLO+GAL	GPS+GLO	GPS+BDS	GPS+GAL
Acquisition <sup>3</sup>	Cold start	24 s	25 s	26 s	28 s	30 s
	Hot start	1 s	1 s	1 s	1 s	1 s
	Aided start <sup>4</sup>	2 s	2 s	2 s	2 s	2 s
Nav. update rate	PVT	10 Hz	15 Hz	20 Hz	20 Hz	20 Hz
Sensitivity <sup>5</sup>	Tracking and Nav.	-167 dBm	-167 dBm	-166 dBm	-166 dBm	-167 dBm
	Reacquisition	-160 dBm	-160 dBm	-160 dBm	-160 dBm	-160 dBm
	Cold start	-148 dBm	-148 dBm	-148 dBm	-148 dBm	-148 dBm
	Hot start	-157 dBm	-157 dBm	-157 dBm	-156 dBm	-156 dBm
Position accuracy	PVT	2.0 m CEP	2.0 m CEP	2.0 m CEP	2.0 m CEP	2.0 m CEP

**Table 1: NEO-M9N performance in multi-constellation GNSS modes**

GNSS		GPS	GLONASS	BEIDOU	GALILEO
Acquisition <sup>3</sup>	Cold start	29 s	30 s	34 s	45 s
	Hot start	1 s	1 s	1 s	1 s
	Aided start <sup>4</sup>	2 s	2 s	2 s	2 s

<sup>1</sup> Assuming Airborne 4 g platform

<sup>2</sup> 50% @ 30 m/s for dynamic operation

<sup>3</sup> Commanded starts. All satellites at -130 dBm, except Galileo at -127 dBm. GPS always in combination with QZSS and SBAS. Measured at room temperature.

<sup>4</sup> Dependent on the speed and latency of the aiding data connection, commanded starts

<sup>5</sup> Demonstrated with a good external LNA

GNSS		GPS	GLONASS	BEIDOU	GALILEO
Nav. update rate	PVT	30 Hz	30 Hz	30 Hz	30 Hz
Sensitivity <sup>5</sup>	Tracking and Nav.	-166 dBm	-166 dBm	-160 dBm	-159 dBm
	Reacquisition	-160 dBm	-156 dBm	-157 dBm	-153 dBm
	Cold start	-148 dBm	-145 dBm	-146 dBm	-140 dBm
	Hot start	-156 dBm	-155 dBm	-155 dBm	-151 dBm
Position accuracy	PVT	2.0 m CEP	4.0 m CEP	3.0 m CEP	3.0 m CEP

**Table 2: NEO-M9N performance in single GNSS modes**

## 1.3 Supported GNSS constellations

The NEO-M9N is a concurrent GNSS receiver which can receive and track multiple GNSS systems. Owing to the multi-band RF front-end architecture all four major GNSS constellations, GPS, Galileo, GLONASS and BeiDou can be received concurrently. The NEO-M9N receiver can be configured for concurrent GPS, GLONASS, Galileo and BeiDou plus SBAS and QZSS reception. If power consumption is a key factor, then the receiver can be configured for a sub-set of GNSS constellations.

The NEO-M9N supports the GNSS and their signals as shown in [Table 3](#).

GPS	GLONASS	Galileo	BeiDou
L1C/A (1575.42 MHz)	L1OF (1602 MHz + $k \cdot 562.5$ kHz, $k = -7, \dots, 5, 6$ )	E1-B/C (1575.42 MHz)	B1I (1561.098 MHz)

**Table 3: Supported GNSS and signals on NEO-M9N**

The following GNSS assistance services can be activated on NEO-M9N:

AssistNow™ Online	AssistNow™ Offline	AssistNow™ Autonomous
Supported	Supported	Supported

**Table 4: Supported Assisted GNSS (A-GNSS) Services**

NEO-M9N supports the following augmentation systems:

SBAS	QZSS	IMES	Differential GNSS
EGNOS, GAGAN, MSAS and WAAS supported	L1s (SAIF) supported	Not supported	RTCM 3.3

**Table 5: Supported Augmentation Systems of NEO-M9N**


The augmentation systems SBAS and QZSS can be enabled only if GPS operation is also enabled.

## 1.4 Supported protocols

The NEO-M9N supports the following protocols:

Protocol	Type
UBX	Input/output, binary, u-blox proprietary
NMEA 4.10	Input/output, ASCII
RTCM 3.3	Input/output, binary

**Table 6: Supported protocols**

For specification of the protocols, see the u-blox NEO-M9N Interface Description [2].

## 1.5 Firmware features

Feature	Comment
Power save modes	On/off, cyclic
Backup modes	Hardware backup mode, software backup mode
Data batching	Autonomous tracking up to 10 min
Data-logger	Position, velocity, time, and odometer data
Odometer feature	Measure traveled distance with support for different user profiles
Geo-fencing	Up to 4 circular areas
Upgradeable firmware	Firmware can be upgraded via download from host or updated in the internal flash memory.

**Table 7: Firmware features**

Feature	Comment
Anti-jamming	RF interference and jamming detection and reporting; Active GNSS in-band filtering
Anti-spoofing	Spoofing detection and reporting; Spoofing blocking using GALILEO OS-NMA
Message integrity	All messages signed with SHA-256
Configuration lockdown	Receiver configuration can be locked by command
Secure boot	Only signed FW images executed

**Table 8: Security features**

## 2 System description

### 2.1 Block diagram

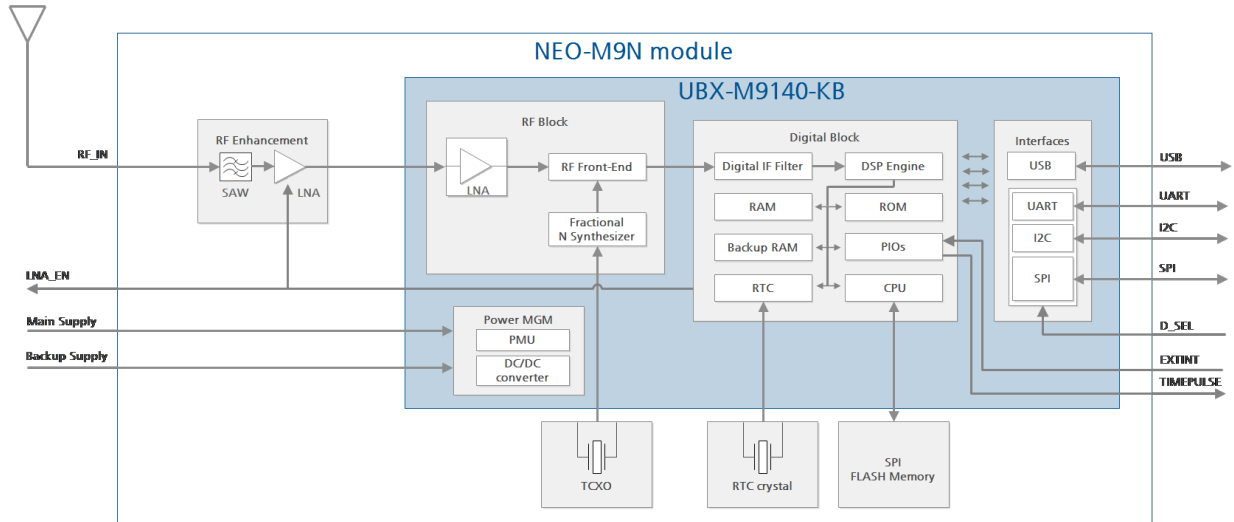


Figure 1: NEO-M9N block diagram

## 3 Pin definition

### 3.1 Pin assignment

The pin assignment of the NEO-M9N module is shown in [Figure 2](#). The defined configuration of the PIOs is listed in [Table 9](#).

For detailed information on pin functions and characteristics, see the u-blox NEO-M9N Integration Manual [1].

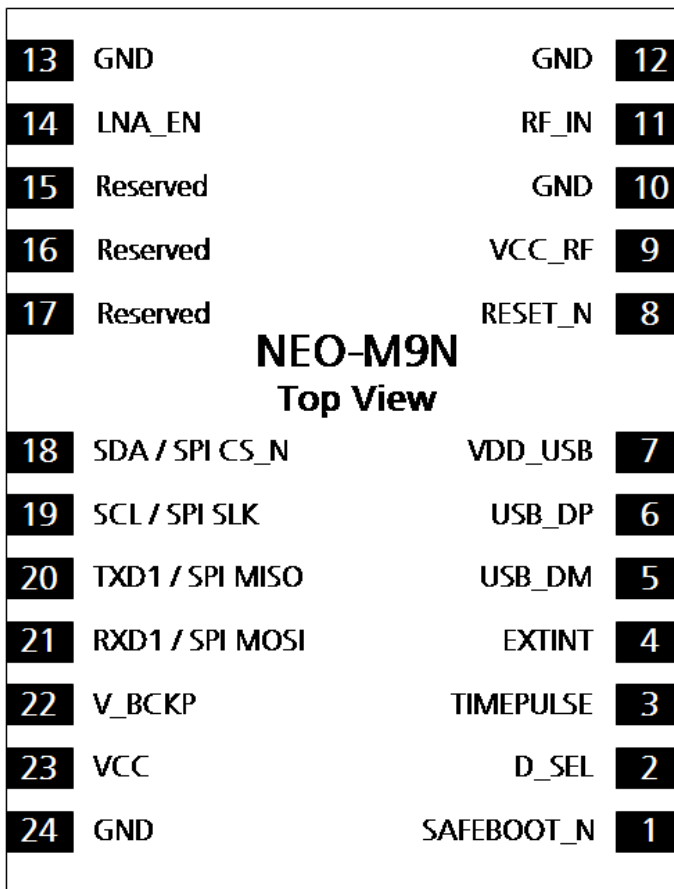


Figure 2: NEO-M9N pin assignment

Pin No	Name	I/O	Description
1	SAFEBOOT_N	I	SAFEBOOT_N (for future service, updates and reconfiguration, leave open)
2	D_SEL	I	Interface select (open or 1 = UART + I2C; 0 = SPI)
3	TIMEPULSE	O	TIMEPULSE (1PPS)
4	EXTINT	I	EXTINT
5	USB_DM	I/O	USB Data (DM)
6	USB_DP	I/O	USB Data (DP)
7	VDD_USB	I	USB Supply
8	RESET_N	I	RESET (active low)
9	VCC_RF	O	External LNA power






Pin No	Name	I/O	Description
10	GND	I	Ground
11	RF_IN	I	GNSS signal input
12	GND	I	Ground
13	GND	I	Ground
14	LNA_EN	O	External LNA enable
15	Reserved	-	Reserved
16	Reserved	-	Reserved
17	EXTINT	I	External interrupt pin
18	SDA / SPI CS_N	I/O	I2C data if D_SEL = 1 (or open); SPI chip select if D_SEL = 0
19	SCL / SPI SLK	I/O	I2C clock if D_SEL = 1 (or open); SPI clock if D_SEL = 0
20	TXD1 / SPI MISO	O	UART output if D_SEL = 1 (or open); SPI MISO if D_SEL = 0
21	RXD1 / SPI MOSI	I	UART input if D_SEL = 1 (or open); SPI MOSI if D_SEL = 0
22	V_BCKP	I	Backup voltage supply
23	VCC	I	Supply voltage
24	GND	I	Ground

**Table 9: NEO-M9N pin assignment**



For detailed information on the pin functions and characteristics see the u-blox NEO-M9N Integration Manual [1].


## 4 Electrical specification

-  The limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only. Operation of the device at these or at any other conditions above those given below is not implied. Exposure to limiting values for extended periods may affect device reliability.
-  Where application information is given, it is advisory only and does not form part of the specification.
-  For detailed information on the device integration, see the u-blox NEO-M9N Integration Manual [1].


### 4.1 Absolute maximum ratings

Parameter	Symbol	Condition	Min	Max	Units
Power supply voltage	VCC		-0.5	3.6	V
Backup battery voltage	V_BCKP		-0.5	3.6	V
Input pin voltage	V <sub>in</sub>	VCC ≤ 3.1V	-0.5	VCC+0.5	V
		VCC > 3.1V	-0.5	3.6	V
DC current through any digital I/O pin (except supplies)	I <sub>pin</sub>			TBD	mA
VCC_RF output current	ICC_RF			100	mA
Supply voltage USB	VDD_USB		-0.5	3.6	V
USB signals	USB_DN, USB_DP		-0.5	VDD_USB + 0.5	V
Input power at RF_IN	Pr <sub>fin</sub>	source impedance = 50 Ω, continuous wave		10	dBm
Storage temperature	T <sub>stg</sub>		-40	+85	°C

**Table 10: Absolute maximum ratings**

-  The product is not protected against overvoltage or reversed voltages. If necessary, voltage spikes exceeding the power supply voltage specification, given in the table above, must be limited to values within the specified boundaries by using appropriate protection diodes.

### 4.2 Operating conditions

-  All specifications are at an ambient temperature of 25 °C. Extreme operating temperatures can significantly impact specification values. Applications operating near the temperature limits should be tested to ensure the specification.

Parameter	Symbol	Min	Typical	Max	Units	Condition
Power supply voltage	VCC	2.7		3.6	V	
Backup battery voltage	V_BCKP	1.65		3.6	V	
Backup battery current	I_BCKP		TBD		μA	
SW backup current	I_SWBCKP		TBD		mA	
Input pin voltage range	V <sub>in</sub>	0		VCC	V	
Digital IO pin low level input voltage	V <sub>il</sub>	0		0.4	V	
Digital IO pin high level input voltage	V <sub>ih</sub>	2.4		VCC+0.3	V	VCC ≤ 3.3V
		2.4		VCC	V	VCC > 3.3V
Digital IO pin low level output voltage	V <sub>ol</sub>			0.4	V	I <sub>ol</sub> = 2 mA

Parameter	Symbol	Min	Typical	Max	Units	Condition
Digital IO pin high level output voltage	Voh	VCC – 0.4			V	Ioh = 2 mA
VCC_RF voltage	VCC_RF	TBD	TBD	TBD	V	
VCC_RF output current	ICC_RF			TBD	mA	
Receiver chain noise figure	NFtot		TBD		dB	
Operating temperature	Topr	-40	+25	85	°C	

**Table 11: Operating conditions**

Operation beyond the specified operating conditions can affect device reliability.

### 4.3 Indicative power requirements

Table 12 lists examples of the total system supply current including RF and baseband section for a possible application.

Values in Table 12 are provided for customer information only, as an example of typical current requirements. Values are characterized on samples with a commanded cold start. Actual power requirements can vary depending on FW version used, external circuitry, number of SVs tracked, signal strength, type and time of start, duration, and conditions of test.

Symbol	Parameter	Conditions	GPS+GLO+GAL+BDS	GPS+GLO	GPS	Unit
I <sub>PEAK</sub>	Peak current	Acquisition	100	100	100	mA
I <sub>VCC</sub> <sup>6</sup>	VCC current	Acquisition	36	33	27	mA
		Tracking (Continuous mode)	36	33	27	mA
		Tracking (Power save mode)	TBD	TBD	TBD	mA

**Table 12: Currents to calculate the indicative power requirements**

All values in Table 12 are measured at 25 °C ambient temperature. SBAS and QZSS is activated in all measurements.

<sup>6</sup> Simulated signal, current measured at 3.0 V

## 5 Communications interfaces

There are several communications interfaces including UART, SPI, I2C and USB.

All the inputs have internal pull-up resistors in normal operation and can be left open if not used. All the PIOs are supplied by VCC, therefore all the voltage levels of the PIO pins are related to VCC supply voltage.

### 5.1 UART interface

There is one UART interface which operates up to and including a speed of 921600 baud. No hardware flow control is supported.

UART is enabled by default if D\_SEL = 1 or open.

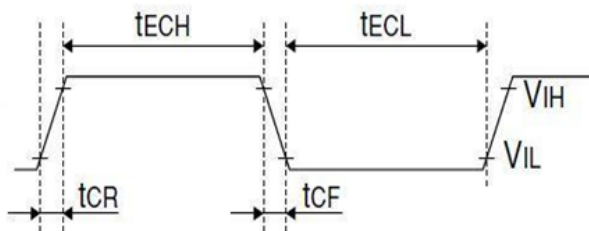


Figure 3: NEO-M9N module UART timing specifications

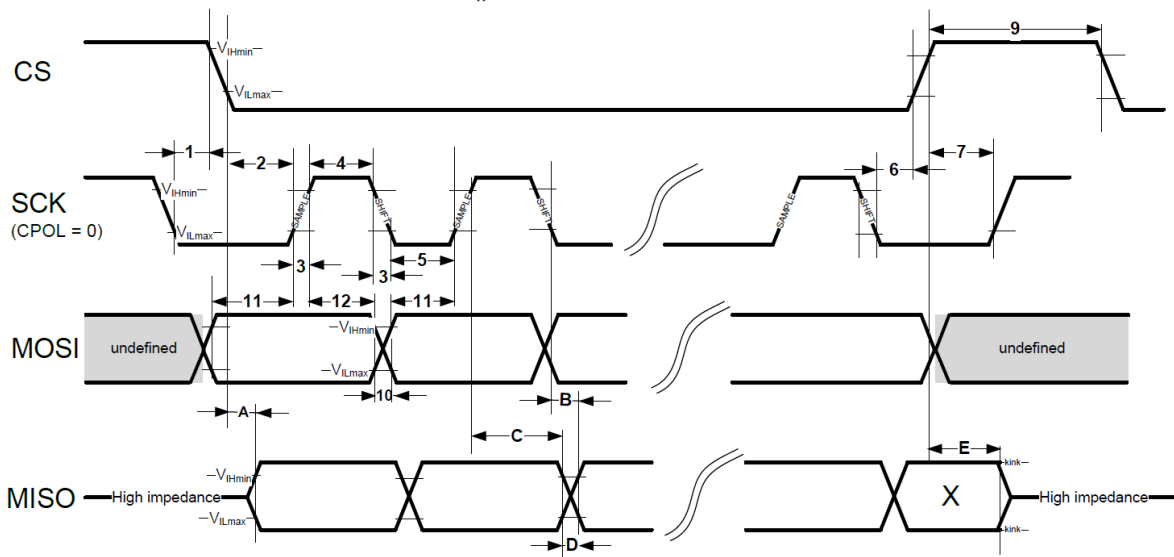
Symbol	Parameter	Min	Max	Unit
$t_{ECH}$	HIGH period of external data input	0	0.4	$\mu s$
$t_{ECL}$	LOW period of external data input	TBA	TBA	$\mu s$
$R_u$	Baudrate	4800	921600	bd
$t_{CR}$	Rise time of data		5	ns
$t_{CF}$	Fall time of data		5	ns

Table 13: NEO-M9N UART timings and specifications

### 5.2 SPI interface

The NEO-M9N has an SPI slave interface that can be selected by setting D\_SEL = 0 and used to communicate to a host. The SPI pins available are: SPI\_MISO, SPI\_MOSI, SPI\_CS\_N, SPI\_CLK. The SPI interface is designed to allow communication to a host CPU. The interface can be operated in slave mode only. Note that SPI is not available in the default configuration because its pins are shared with the UART and DDC interfaces. The maximum transfer rate using SPI is 125 kB/s and the maximum SPI clock frequency is 5.5 MHz.

This section provides SPI timing values for the NEO-M9N slave operation. The following tables present timing values under different capacitive loading conditions. Default SPI configuration is CPOL = 0 and CPHA = 0.


**Figure 4: NEO-M9N module SPI specification Mode 1: CPHA=0 SCK = 5.33 MHz**


Timings 1 - 12 are not specified here.

Timing value @ 2 pF load	Min (ns)	Max (ns)
"A" - MISO data valid time (CS)	14	38
"B" - MISO data valid time (SCK) weak driver mode	21	38
"C" - MISO data hold time	114	130
"D" - MISO rise/fall time, weak driver mode	1	4
"E" - MISO data disable lag time	20	32

**Table 14: NEO-M9N SPI timings @ 2pF load**

Timing value @ 20 pF load	Min (ns)	Max (ns)
"A" - MISO data valid time (CS)	19	52
"B" - MISO data valid time (SCK) weak driver mode	25	51
"C" - MISO data hold time	117	137
"D" - MISO rise/fall time, weak driver mode	6	16
"E" - MISO data disable lag time	20	32

**Table 15: NEO-M9N SPI timings @ 20pF load**

Timing value @ 60 pF load	Min (ns)	Max (ns)
"A" - MISO data valid time (CS)	29	79
"B" - MISO data valid time (SCK) weak driver mode	35	78
"C" - MISO data hold time	122	152
"D" - MISO rise/fall time, weak driver mode	15	41
"E" - MISO data disable lag time	20	32

**Table 16: NEO-M9N SPI timings @ 60pF load**

## 5.3 Slave I2C interface

An I2C compliant DDC interface is available for communication with an external host CPU. The interface can be operated in slave mode only. The DDC protocol and electrical interface are fully compatible with Fast-Mode of the I2C industry standard. Since the maximum SCL clock frequency

is 400 kHz, the maximum bit rate is 400 kbit/s. The interface stretches the clock when slowed down while serving interrupts, therefore the real bit rates may be slightly lower.



The I2C interface is only available with the UART default mode. If the SPI interface is selected by using  $D\_SEL = 0$ , the I2C interface is not available.

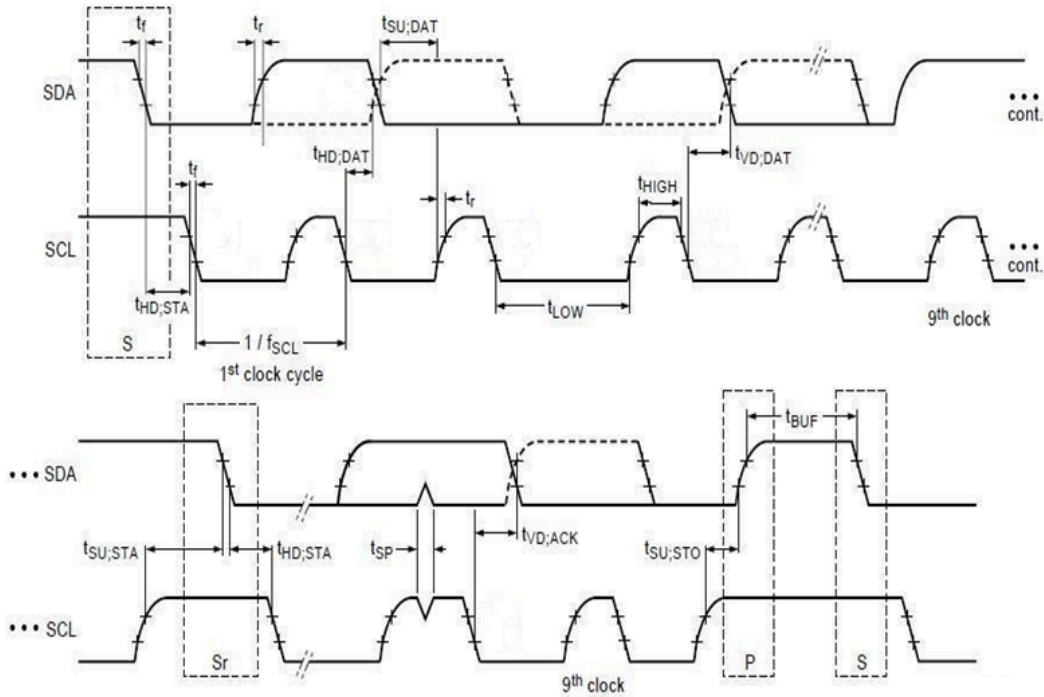


Figure 5: NEO-M9N module I2C slave specification

Symbol	Parameter	Min	Max	Unit
$f_{SCL}$	SCL clock frequency	0	400	kHz
$t_{HD,STA}$	Hold time (repeated) START condition	4.0/1	-	$\mu s$
$t_{LOW}$	LOW period of the SCL clock	5/2	-	$\mu s$
$t_{HIGH}$	HIGH period of the SCL clock	4.0/1	-	$\mu s$
$t_{SU,STA}$	Set-up time for a repeated START condition	5/1	-	$\mu s$
$t_{HD,DAT}$	Data hold time	0/0	-	$\mu s$
$t_{SU,DAT}$	Data set-up time	250/100	-	$\mu s$
$t_r$	Rise time of both SDA and SCL signals	-	1000/300 (for C 400pF)	$\mu s$
$t_f$	Fall time of both SDA and SCL signals	-	300/300 (for C 400pF)	$\mu s$
$t_{SU,STO}$	Set-up time for STOP condition	4.0/1	-	$\mu s$
$t_{BUF}$	Bus free time between a STOP and START condition	5/2	-	$\mu s$
$t_{VD,DAT}$	Data valid time	-	4/1	$\mu s$
$t_{VD,ACK}$	Data valid acknowledge time	-	4/1	$\mu s$
$V_{nL}$	Noise margin at the LOW level	0.1VCC	-	V
$V_{nH}$	Noise margin at the HIGH level	0.2VCC	-	V

Table 17: NEO-M9N I2C Slave timings and specifications

## 5.4 USB interface

A USB interface, which is compatible to USB version 2.0 FS (Full Speed, 12 Mbit/s), can be used for communication to a host. The VDD\_USB pin supplies the USB interface.

## 5.5 Default interface settings

Interface	Settings
UART	9600 Baud, 8 bits, no parity bit, 1 stop bit. Output messages: NMEA <b>GGA</b> , <b>GLL</b> , <b>GSA</b> , <b>GSV</b> , <b>RMC</b> , <b>VTG</b> , <b>TXT</b> (no UBX). Input protocols: UBX, NMEA and RTCM 3.3.
USB	Output messages activated as in UART. Input protocols available as in UART.
I2C	Output messages activated as in UART. Input protocols available as in UART.
SPI	Output messages activated as in UART. Input protocols available as in UART.

**Table 18: Default interface settings**



Refer to the u-blox NEO-M9N Interface Description [2] for information about further settings.

By default the NEO-M9N outputs NMEA 4.10 messages that include satellite data for all GNSS bands being received. This results in a higher-than-before NMEA load output for each navigation period. Make sure the UART baud rate being used is sufficient for the selected navigation rate and the number of GNSS signals being received.

## 6 Mechanical specification

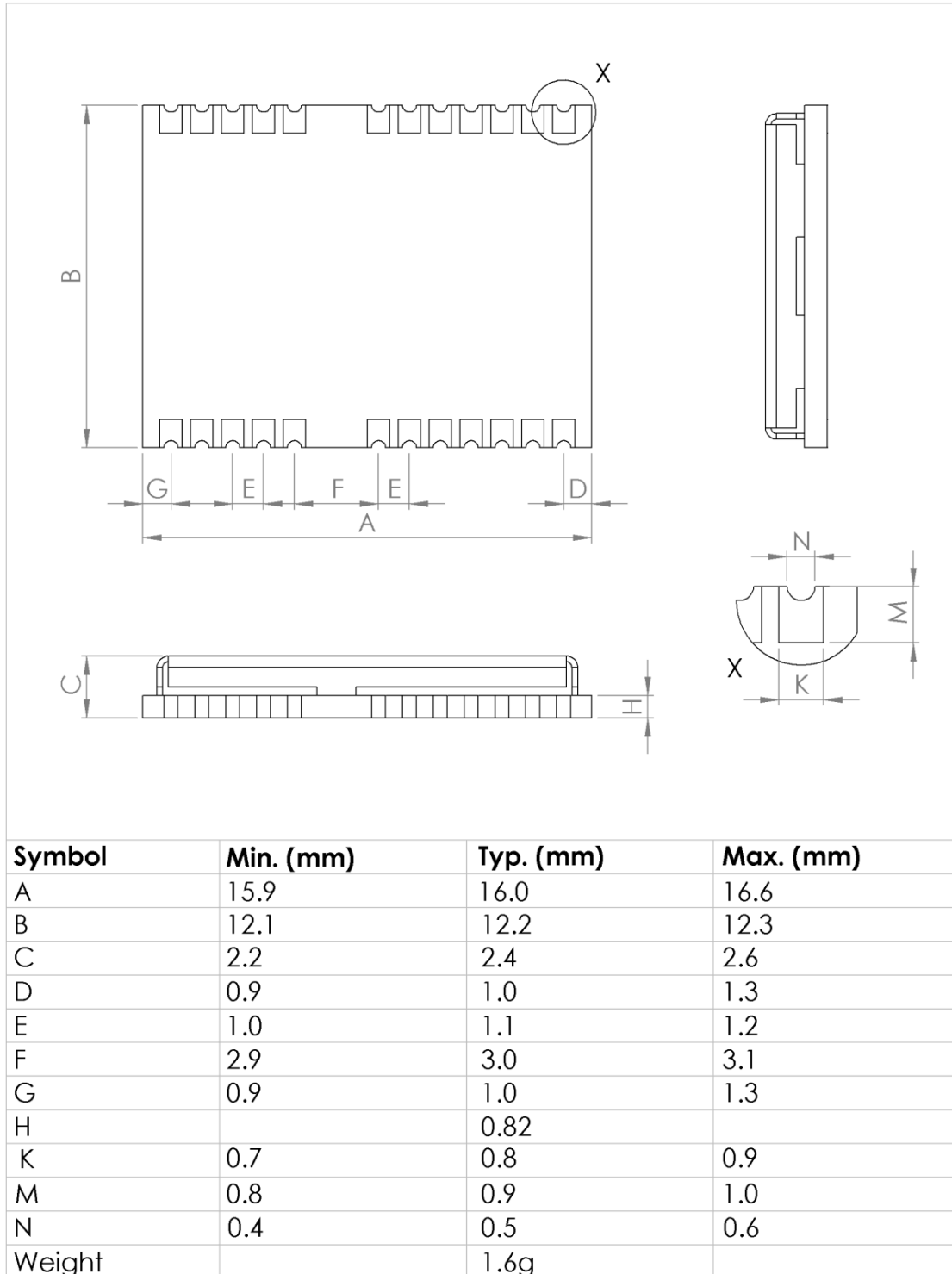


Figure 6: NEO-M9N mechanical drawing



## 7 Reliability tests and approvals

All u-blox modules are based on AEC-Q100 qualified GNSS chips.

Tests for product family qualifications are according to ISO 16750 "Road vehicles – environmental conditions and testing for electrical and electronic equipment", and appropriate standards.

### 7.1 Approvals



The NEO-M9N is designed to in compliance with the essential requirements and other relevant provisions of Radio Equipment Directive (RED) 2014/53/EU.

The NEO-M9N complies with the Directive 2011/65/EU (EU RoHS 2) and its amendment Directive (EU) 2015/863 (EU RoHS 3).

Declaration of Conformity (DoC) is available on the [u-blox website](#).

## 8 Labeling and ordering information

### 8.1 Product labeling

The labeling of the NEO-M9N modules provides product information and revision information. For more information contact sales.

### 8.2 Explanation of product codes

Three different product code formats are used. The **Product Name** is used in documentation such as this data sheet and identifies all u-blox products, independent of packaging and quality grade. The **Ordering Code** includes options and quality, while the **Type Number** includes the hardware and firmware versions. [Table 19](#) below details these three different formats.

Format	Structure
Product Name	NEO-M9N
Ordering Code	NEO-M9N-00B
Type Number	NEO-M9N-00B-00

Table 19: Product code formats

### 8.3 Ordering codes

Ordering No.	Product
UBX-M9N-00B	u-blox NEO-M9N module, professional grade

Table 20: Product ordering codes



Product changes affecting form, fit or function are documented by u-blox. For a list of Product Change Notifications (PCNs) see our website at: <https://www.u-blox.com/en/product-resources>.

## Related documents

- [1] NEO-M9N Integration Manual, doc. No. UBX-19014286
- [2] NEO-M9N Interface Description, doc. No. UBX-18053584



For regular updates to u-blox documentation and to receive product change notifications please register on our homepage (<http://www.u-blox.com>).

## Revision history

Revision	Date	Name	Status / Comments
R01	15-Aug-2019	jesk	Objective Specification

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